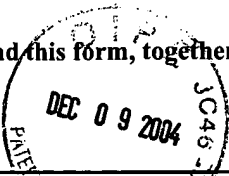


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Patricia Wiggins	(Depositor's name)
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12/13/2004 HUUONG62 00000102 041679 10755496

01 FC:1504 300.00 DP
 02 FC:1501 30.00 DA 1370.00 DP

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/755,496	01/12/2004	Shao-Yu Chou	TS00-577C	1280

TITLE OF INVENTION: POWER-ON RESET CIRCUIT FOR ERASING OF SPLIT GATE FLASH MEMORY REFERENCE CELLS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$300	\$1670	01/21/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
MAI, SON LUU	2818	365-185200

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☒ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 Duane Morris LLP

2 _____

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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Taiwan Semiconductor Manufacturing Company, Hsin-Chu TaiwanPlease check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

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Authorized Signature

Howard Chen

Date

12/9/04

Typed or printed name

Registration No.

46,615

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